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Poznan University of Tech Faculty of Electrical E	0,	Europ	bean Credit Transfer System
	STUDY MODULE DE	ESCRIPTION FORM	
Name of the module/subject C			ode 010321331010323752
Field of study		Profile of study (general academic, practical)	Year /Semester
Electrical Engineering	ng	(brak)	2/3
Elective path/specialty		Subject offered in:  Polish	Course (compulsory, elective) <b>obligatory</b>
Cycle of study:		Form of study (full-time,part-time)	
First-cycle studies full-time		ne	
No. of hours			No. of credits
Lecture: 30 Classe	s: - Laboratory: -	Project/seminars:	2
Status of the course in the study	program (Basic, major, other)	(university-wide, from another fiel	d)
	(brak)	(b	rak)
Education areas and fields of science and art		·	ECTS distribution (number and %)
technical sciences			2 100%
Technical sciences			2 100%
Responsible for subj	ect / lecturer:		
dr hab. inż. Michał Gwóźc email: michal.gwozdz@p tel. 61 665 2646 Faculty of Electrical Engi ul. Piotrowo 3A 60-965 P	ut.poznan.pl		
Prerequisites in term	ns of knowledge, skills and	d social competencies:	
Knowledge  Basic knowledge of physics, electrical engineering, and mathematical analysis			

1	Knowledge	Basic knowledge of physics, electrical engineering, and mathematical analysis
2	Skills	Analysis and synthesis of circuits, carrying out activities in the field of basic account of operator. The ability to effectively self-education in a field related to the chosen field of study
3	Social competencies	It is aware of the need to broaden their competence, willingness to cooperate within the team

# Assumptions and objectives of the course:

Getting to know the construction, parameters and applications of basic electronic components. Getting to know the principles of operation of analog and digital electronic circuits. Skills in electronic design at the basic level.

# Study outcomes and reference to the educational results for a field of study

### Knowledge:

- 1. Can describe the operating principles and parameters of basic electronic components, characterize the structure and use of basic analog and digital electronic circuits - [K\_W04 + K\_W07 + K\_W14 +++]]
- 2. Can describe the basic criteria for the design of electronic circuits [K W04 + K W14 +++]

### Skills:

- 1. Knows how to apply the knowledge in electronics to analyze the operation of basic analog and digital electronic circuits -[K\_U01 + K\_U03 ++]
- 2. Can define the criteria necessary for the proper design of electronic system-level [K\_U01 ++ K\_U03 +]

#### Social competencies:

1. Is able to think and act in an entrepreneurial way in the area of electronic design - [K\_K02 ++]

# Assessment methods of study outcomes Assessment of the knowledge and skills shown on the written test **Course description**

## **Faculty of Electrical Engineering**

Updated 2017. The lecture with multimedia presentation (drawings, equations, basic content) supplemented by the content on the blackboard. The properties and characteristics of the basic elements and electronic devices: passive components, semiconductor diodes, bipolar transistors - their operation and application. Semiconductor optoelectronic devices - their properties and application examples. The feedback in analog circuits. Operational amplifiers - the ideal and the real, properties, parameters, applications. Power amplifiers - properties and applications. Electronics generators: excitation conditions, types and application generators. Linear circuits. Analog filters: types, design rules, and the use of filters. Basics of digital technique: binary system of writing numbers, logic states, logical operations, elements of logic, truth table, digital combinational and sequential circuits. TTL Family. Semiconductor memories: general classification, basic properties of certain types of memory.

### Basic bibliography:

- 1. P. Horowitz, W. Hill, Sztuka elektroniki. Część 1 i 2, WKŁ, 2014
- 2. Z. Kulka, M. Nadachowski, Analogowe układy scalone, WKŁ, W-wa, 1980
- 3. P. Górecki, Wzmacniacze operacyjne, Wydawnictwo BTC, Warszawa, 2004
- 4. J. Kalisz, Podstawy elektroniki cyfrowej, WKiŁ, Warszawa, 2002

#### Additional bibliography:

- 1. M. P. Kaźmierkowski, J. T. Matysik, Wprowadzenie do elektroniki i energoelektroniki, Oficyna Wyd. PW, Warszawa, 2005
- 2. Michał Gwóźdź, Dominik Matecki, Power Electronics Inverter with a Modified Sigma-Delta Modulator and an Output Stage Based on GaN E-HEMTs, Advanced Control of Electrical Drives and Power Electronic Converters, Springer, London, ISBN: 978-3-319-45735-2, pp. 327-338

#### Result of average student's workload

Activity	Time (working hours)
1. Participation in lecture classes	30
2. Participation in consultations	5
3. Przygotowanie do egzaminu	15

#### Student's workload

Source of workload	hours	ECTS
Total workload	50	2
Contact hours	35	1
Practical activities	0	0